

MODIFICATION OF STANDARD KEPCO MODEL BOP 50-4MC KEPCO MODEL BOP 50-4DC

The Kepco Models BOP 50-4MC and BOP 50-4DC have been modified from standard Kepco Models BOP 50-4M and BOP 50-4D, respectively, to be stable handling capacitive loads up to 10 mF.

INSTRUCTION MANUAL CORRECTIONS:

This modification makes the BOP more suitable for a wide variety of applications such as solar cell/ solar panel testing, piezoelectric device driving/ testing, capacitor testing, capacitive transducer driving/testing and powering industrial or lab-type applications for capacitive or capacitive-resistive loads.

Static specifications representing the unit's accuracy in Voltage mode are identical with the standard BOP models, while the ripple and noise specifications are better (approximately 50% lower) for MC and DC models than standard BOP models.

Specifications listed in Table 1 are for BOP 50-4MC and BOP 50-4DC in Voltage Mode.

TABLE 1. BOP 50-4MC AND BOP 50-4DC SPECIFICATIONS

| Bandwidth | (DC to f-3dB) | Rise/Fall Time (2) | Recovery at Step Load (3) | |
|-------------------------|--------------------------------------|--------------------|---------------------------|--|
| Resistive Load, Nominal | Capacitive Load, 10μF ⁽¹⁾ | Rise/Faii Tillie V | | |
| 11.5 kHz | 15 kHz | 35 μS | 100 μS | |

NOTES:

- (1) Nonuniformities of the frequency response for the standard $10\mu F$ load create a larger 3-dB bandwidth than for the resistive load.
- (2) 10% to 90%, with nominal resistive load.
- (3) Load between infinity and nominal resistive load values.

In voltage mode the bandwidth of the BOP 50-4MC and DC models is less than the standard BOP, while the response time is increased. Nonuniformities of the frequency response can be nearly eliminated by reducing the bandwidth in Voltage mode. Bandwidth can be reduced in a predictable manner by increasing the internal compensation capacitance by placing an external capacitor in parallel across C21 of Assembly A1 (see Table 2 and Figure 1).

TABLE 2. BANDWIDTH CORRECTION

| | EXTERNAL CAPACITOR (ACROSS A1C21, SEE FIGURE 1) | | | | | | | |
|---------------------|--|--------|--------|-------|---------|-------|---------|--|
| | 1 nF | 2.2 nF | 4.7 nF | 15 nF | 33 nF | 47 nF | 100 nF | |
| CORRECTED BANDWIDTH | 11 kHz | 9 kHz | 7 kHz | 3 kHz | 1.5 kHz | 1 kHz | 0.5 kHz | |

NOTE: Listed bandwidth correction values are for Voltage mode, nominal resistive load.

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In Current Mode the dynamic specifications are almost identical for all BOP MC and DC models: 3-dB bandwidth of 4.9kHz and rise/ fall time of $72\mu s$ (lower bandwidth and higher rise/ fall time than the standard BOP M and D models).

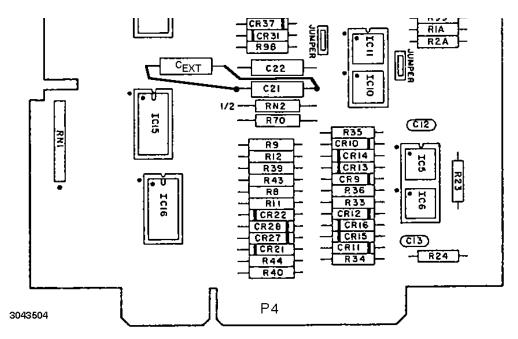


FIGURE 1. ASSEMBLY A1, EXTERNAL BANDWIDTH CORRECTION CAPACITOR LOCATION

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